

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO : 7,073,100

DATED : July 4, 2006

INVENTOR(S) : Chadwick *et al.*

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Front Page

Under (75) Inventors, insert --Brian R. Kessler, Richmond, VT (US); --

MAILING ADDRESS OF SENDER: Jack P. Friedman, Ph.D.

Reg. No. 44,688

22 Century Hill Drive, Suite 302

Latham, NY 12110

PATENT NO. 7,073,100

No. of additional copies



This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing the burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Chadwick et al.

Docket No.: BUR920010195US1

Serial No.: 10/065,694

Art Unit: 2133

Filed: 11/11/2002

Examiner: Guy Lamarre

Patent No. 7,073,100

Date of Patent: July 4, 2006

Title: METHOD FOR TESTING EMBEDDED DRAM ARRAY

Commissioner for Patents
Washington, D.C. 20231

**DOCUMENTS INCLUDED TO SUBSTANTIATE THE ENCLOSED CERTIFICATE OF
CORRECTION**

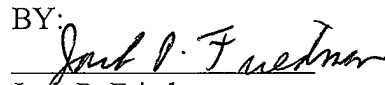
The following documents are included which show that the United States Patent and Trademark Office had previously received the documents necessary to correct Inventorship by adding Brian R. Kessler as an inventor in the above named patent:

Petition Under C. F. R. 1.48
Statement of Newly Named Inventor (Brian R. Kessler)
New Declaration signed by All Inventors
Assent of Assignee to correction and/or addition of Inventor(s)
Statement Under 37 C. F. R. 3.73(b)

The receipt from the United States Patent Office Tech Center 2100 dated 02/11/03 showing the documents had been received is also enclosed.

Respectfully submitted,
FOR:
Anthony M. Palagonia
Registration No.: 41,237

Dated: 08/11/2006

BY: 
Jack P. Friedman
Reg. No. 44,688

Schmeiser, Olsen & Watts
22 Century Hill Drive, Suite 302
Latham, New York 12110
(518) 220-1850
Agent Direct Dial Number: (802)-899-5460

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Chadwick et al.

Docket No.: BUR920010195US1

Serial No.: 10/065,694

Art Unit: Unknown

Filed: 11/11/2002

Examiner: Unknown

Title: METHOD FOR TESTING EMBEDDED DRAM ARRAYS

PETITION UNDER C. F. R § 1.48

Commissioner for Patents
Washington, D.C. 20231

Applicants petition to correct Inventorship of above referenced application. Applicants contend that Brian R. Kessler is a true inventor of the above referenced application and was unintentionally left off the declaration without deceptive intent and Applicants hereby request entry of the attached new declaration.

Attached:

Statement from Brian R. Kessler

New Declaration Signed by All Inventors

State from Assignee Consenting to New Declaration

Assignment- *JO*

Assignment Recordation Fee- *JO*

Petition Fee under C. F. R. § 1.17(h)

Respectfully submitted,

FOR:

Anthony M. Palagonia

Registration No.: 41,237

Dated: 12/12/2002

BY: *Jack P. Friedman*
Jack P. Friedman
Reg. No. 44,688

3 Lear Jet Lane, Suite 201
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Latham, New York 12110
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Chadwick et al.

Docket No.: BUR920010195US1

Serial No.: 10/065,694

Art Unit: Unknown

Filed: 11/11/2002

Examiner: Unknown

Title: METHOD FOR TESTING EMBEDDED DRAM ARRAYS

STATEMENT OF NEWLY NAMED INVENTOR

Commissioner for Patents
Washington, D.C. 20231

I, Brian R. Kessler declare that I am a true inventor of the above referenced application
and that my name was unintentionally left off the declaration without deceptive intent.

Respectfully submitted,
Brian R. Kessler

Dated: 1/29/03

Brian R. Kessler

Richmond, VT 05477

Brian R. Kessler

Docket No.
BUR920010195US1

Declaration and Power of Attorney For Patent Application

English Language Declaration

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled
METHOD FOR TESTING EMBEDDED DRAM ARRAYS

the specification of which

(check one)

☐ is attached hereto.

☒ was filed on 11/11/2002 as United States Application No. or PCT International
Application Number 10/065,694
and was amended on _____

(if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119(a)-(d) or Section 365(b) of any foreign application(s) for patent or inventor's certificate, or Section 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate or PCT International application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)

Priority Not Claimed

_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed)	<input type="checkbox"/>
_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed)	<input type="checkbox"/>
_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed)	<input type="checkbox"/>

I hereby claim the benefit under 35 U.S.C. Section 119(e) of any United States provisional application(s) listed below:

_____	_____
(Application Serial No.)	(Filing Date)

_____	_____
(Application Serial No.)	(Filing Date)

_____	_____
(Application Serial No.)	(Filing Date)

I hereby claim the benefit under 35 U. S. C. Section 120 of any United States application(s), or Section 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. Section 112, I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, C. F. R., Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application:

_____	_____	_____
(Application Serial No.)	(Filing Date)	(Status)
		(patented, pending, abandoned)

_____	_____	_____
(Application Serial No.)	(Filing Date)	(Status)
		(patented, pending, abandoned)

_____	_____	_____
(Application Serial No.)	(Filing Date)	(Status)
		(patented, pending, abandoned)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

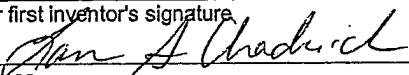
POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. *(list name and registration number)*

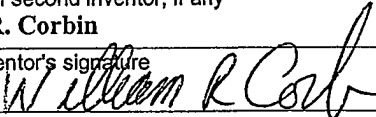
All attorneys and/or agents at Customer No. 30449

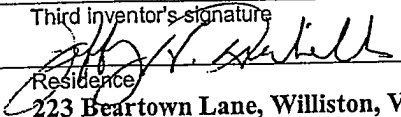
Send Correspondence to: Customer No. 30449

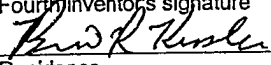
Direct Telephone Calls to: *(name and telephone number)*

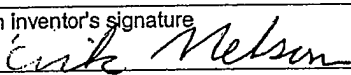
Jack P. Friedman- (518) 220-1850

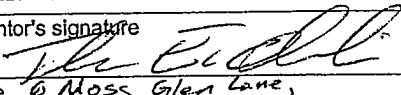
Full name of sole or first inventor Laura S. Chadwick	
Sole or first inventor's signature 	Date 1/24/03
Residence 1 Pomfret Lane, Essex Junction, VT 05452	
Citizenship USA	
Post Office Address Same as Residence	

Full name of second inventor, if any William R. Corbin	
Second inventor's signature 	Date 1/17/03
Residence 19 Butler Road, Underhill, VT 05489	
Citizenship USA	
Post Office Address Same as Residence	

Full name of third inventor, if any Jeffrey H. Dreibelbis	
Third inventor's signature 	Date 1/20/03
Residence 223 Beartown Lane, Williston, VT 05495	
Citizenship USA	
Post Office Address Same as Residence	

Full name of fourth inventor, if any Brain R. Kessler	
Fourth inventor's signature 	Date 1/20/03
Residence 384 Hillview Road, Richmond, VT 05477	
Citizenship USA	
Post Office Address Same as Residence	

Full name of fifth inventor, if any Erik A. Nelson	
Fifth inventor's signature 	Date 1/27/03
Residence 867 Countryside Road, Waterbury, VT 05676	
Citizenship USA	
Post Office Address Same as Residence	

Full name of sixth inventor, if any Thomas E. Obremski	
Sixth inventor's signature 	Date 1/24/03
Residence 6 Moss Glen Lane, TEO 28 Overlook Drive, South Burlington, VT 05403	
Citizenship USA	
Post Office Address Same as Residence	

Full name of seventh inventor, if any Toshiharu Saitoh	1/24/03
Seventh inventor's signature <i>T. Saitoh</i>	Date
Residence 1 Concord Green, South Burlington, VT 05403	
Citizenship	
Post Office Address Same as Residence	

Full name of eighth inventor, if any Donald L. Wheeler	
Eighth inventor's signature <i>Donald L. Wheeler</i>	Date 1/27/03
Residence 564 Buck Hill Road West, Hinesburg, VT 05461	
Citizenship USA	
Post Office Address Same as Residence	

Full name of ninth inventor, if any	
Ninth inventor's signature	Date
Residence	
Citizenship	
Post Office Address	

Full name of tenth inventor, if any	
Tenth inventor's signature	Date
Residence	
Citizenship	
Post Office Address	

Practitioner's Docket No. BUR9-2001-0195US1

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Chadwick et al.

Serial No.: 10/065,694

Group No.: Unknown

Filed: November 11, 2002

Examiner: Unknown

For: Method For Testing Embedded Dram Arrays

Commissioner for Patents
Washington, D.C. 20231

**ASSENT OF ASSIGNEE TO CORRECTION
AND/OR ADDITION OF INVENTOR(S)**

Name of Assignee:

International Business Machines Corporation
New Orchard Road
Armonk, New York 10504

Assignment:

Recorded on November 11, 2002

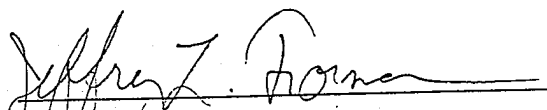
Reel: 013236

Frame: 0676

Assignee hereby assents to the correction of inventorship filed herewith.

ASSIGNEE STATEMENT

A "STATEMENT UNDER 37 C.F.R. 3.73(b)" is attached.



Jeffrey L. Forman
Program Manager, Patent Office Liaison

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

STATEMENT UNDER 37 CFR 3.73(b)

Applicant/Patent Owner: Chadwick et al.

Application No./Patent No.: 10/065,694 Filed/Issue Date: November 11, 2002

Entitled: Method For Testing Embedded Dram Arrays

International Business Machines Corporation, a corporation of New York

(Name of Assignee)

(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☐ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of less than the entire right, title and interest.
The extent (by, percentage) of its ownership interest is _____ %

in the patent application/patent identified above by virtue of either:

- A. ☒ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel 13236, Frame 0676, or for which a copy thereof is attached.

OR

- B. ☐ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

1. From: _____ To: _____

The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

2. From: _____ To: _____

The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

3. From: _____ To: _____

The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

☐ Additional documents in the chain of title are listed on a supplemental sheet.

- ☐ Copies of assignments or other documents in the chain of title are attached.

[NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

February 10, 2003

Date

Jeffrey L. Forman

Typed or printed name

Jeffrey L. Forman

Signature

Docket #BUR9-2001-0195US1

Program Manager, Patent Office Liaison

Title

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

Applicant(s): Chadwick et al.

Docket #BUR9200101950.12

Serial No.: 10/065,694

Filed: November 11, 2002

For: Method For Testing Embedded Dram Arrays

Papers Filed:

- 1) Petition Under C.F.R. 1.48
- 2) Statement of Newly Named Inventor
- 3) New Declaration signed by All Inventors
- 4) Assent of Assignee To Correction And/Or Addition Of Inventor(s)
- 5) Statement Under 37 C.F.R. 3.73(b)
- 6) Check #1800 for \$130.00

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Please acknowledge receipt of the above identified documents by applying the Patent and Trademark Office receipt hereto.